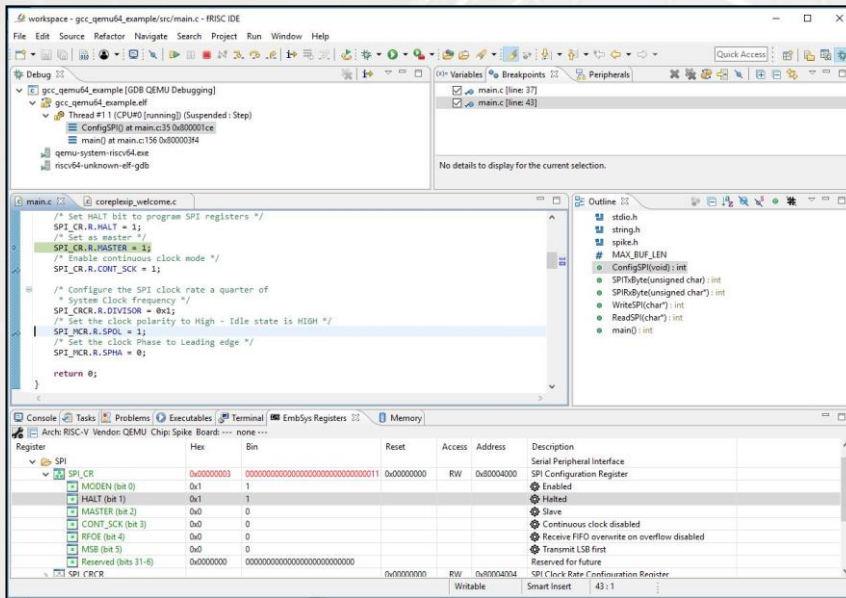


# fRISC Integrated Development Environment for RISC-V



## Overview

Ashling Microsystems, a member of the RISC-V foundation, offers custom embedded development solutions and specialized services for RISC-V.



fRISC is Ashling's Eclipse-based Integrated Development Environment (IDE) for RISC-V.

fRISC provides a complete, seamless environment for RISC-V software development including support for writing, building, simulating and hardware debugging to allow you to *see what your software is hiding*.

fRISC includes a single-shot installer that installs and automatically configures all the component tools to work "out-of-the-box".

## fRISC IDE

- IDE based on Eclipse *Oxygen* release
- RISC-V GCC and LLVM compiler toolchains with full build-tool support
- QEMU ISA simulator for 32-bit/64-bit RISC-V cores using QEMU *Spike* virtual machine
- High-level RISC-V Register Viewer including support for SiFive devices
- OpenOCD hardware debug support
- Integrated RXTX serial terminal
- Integrated FreeRTOS debug support
- Project wizards/templates/examples for RISC-V devices (including SiFive)



## Order Codes

### Product

fRISC IDE for RISC-V Development

### Order Code

fRISC-IDE-RV